

PSE Conformance Test Suite

May 8 2015 5:13 AM

Port Count: 8
 Loop Count: 1
 PSE Tested: Sample Type-2 PSE with LLDP



802.3at Conformance Report

version 4.1.04
 report version 4.1.00

Test Mode: 30 Watt LLDP
 Sifos Interop Index: 93%

Error Log: None

Chassis ID: 192.168.221.103

TestLoop: 1

	PSA-3000 Ports								UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
	1-1	1-2	2-1	2-2	3-1	3-2	4-1	4-2								
Test: det_v																
Open_Circuit_Det_Voc=	10.4	10.4	10.4	10.43	10.38	10.38	10.38	10.38	volts	10.38	10.43	10.39	2.8	Pass	30	Pass
Peak_Det_Vvalid=	7.97	7.97	8	7.98	7.97	7.97	7.95	7.97	volts	7.95	8	8	3.8	Pass	10	Pass
Min_Det_Vvalid=	3.97	4.01	4.03	4	4.02	4.02	4	3.98	volts	3.97	4.03	4	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	3.45	3.41	3.42	3.42	3.4	3.39	3.4	3.44	volts	3.39	3.45	3.4	1	Pass	7.2	Pass
Detection_Slew=	0	0	0	0	0	0	0	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	3	3	3	3	3	3	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.5	0.5	0.6	0.5	0.6	0.6	0.5	0.5	volts	0.5	0.6	0.6	0	Pass	9	Pass
Non_802_Step_V=	0	0	0	0	0	0	0	0	volts	0	0	0	0	Pass	0.1	Pass
High_Sig_MaxV=	10.05	10.07	10.07	10.09	10.07	10.08	10.05	10.08	volts	10.05	10.09	10.1	3.8	Pass	11	Pass
Non_802_Discr_?=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i																
Init_Current_Isc=	0.2	0.19	0.19	0.2	0.2	0.2	0.18	0.18	mA	0.18	0.2	0.19	0	Pass	5	Pass
Det_Current_Isc=	0.14	0.15	0.14	0.15	0.15	0.14	0.13	0.12	mA	0.12	0.15	0.14	0	Pass	5	Pass
Test: det_range																
Rgood_Max=	29	29	29	29	28	29	28	28	Kohm	28	29	28.6	26	Pass	32	Pass
Rgood_Min=	17	17	17	17	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	29	29	29	29	28	29	28	28	Kohm	28	29	28.6	26	Pass	33	Pass
Rgood_Max=	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time																
Backoff_Time_Tdbo=	168	172	168	168	172	168	168	168	msec	168	172	169	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	1200	172	168	168	172	1300	168	168	msec	168	1300	439.5	-1	Pass	1500	Pass
Backoff_Type=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	215	215	219	219	219	219	215	219	msec	215	219	217.5	5	Pass	500	Pass
Total_Det_Time=	219	219	219	223	223	219	219	223	msec	219	223	220.5	5	Pass	1000	Pass
Test: det_resource																
Output_Impedance_Zout=	450	450	368	409.4	410.6	435.4	401.7	401.7	KOhm	368	450	415.9	45	Pass	2000	Pass
Test: class_v																
Class_Voltage_Vclass=	17.7	17.7	17.6	17.2	17.6	17.6	17.6	17.6	volts	17.2	17.7	17.6	15.5	Pass	20.5	Pass
Vclass_Min=	17.5	16.3	17.4	17.1	17.4	17.4	17.4	17.4	volts	16.3	17.5	17.2	15.5	Pass	20.5	Pass
Test: class_time																
Event_Count=	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Class_Time_Tpdc=	11.7	13.7	13.7	13.7	11.7	13.6	11.7	11.7	msec	11.7	13.7	12.7	6	Pass	75	Pass
Test: class_err																
Class_lim=	65	65	65	65	65	65	65	65	mA	65	65	65	51	Pass	100	Pass
Vport_CL_lim=	15.5	14.8	14.8	14.8	14.6	14.8	14.6	15	V	14.6	15.5	14.9	0	Pass	20.5	Pass
Vport_CL_err_l=	17	17	16.9	17	16.9	17	16.9	17	V	16.9	17	17	0	Pass	20.5	Pass
Test: class_lldp																
PSE_Source_Priority=	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_MDL_Pwr_Sup=	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_LLDP_Time_2=	3.1	3.5	3.1	3.1	3.4	3.4	3.4	3.1	sec	3.1	3.5	3.3	0	Pass	10	Pass
PSE_LLDP_Type_2=	2	2	2	2	2	2	2	2	*	2	2	2	2	Pass	2	Pass
PSE_Echo_Time_2=	7	5.8	6.2	6.2	34.9	34	35.3	0.3	sec	0.3	35.3	16.2	0	Pass	10	Fail
PSE_Alloc_Pwr_2=	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	Watts	20.3	20.3	20.3	20.3	Pass	25.5	Pass
PSE_Alloc_Time_2=	7	5.8	6.2	6.2	34.9	34	35.3	0.3	sec	0.3	35.3	16.2	0	Pass	30	Info
PD_Power_Adjust_2=	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	Watts	25.5	25.5	25.5	25.5	Pass	25.5	Pass
PSE_Adjust_Time_2=	6.2	6.2	5.8	4.9	7	2	5.8	4.1	sec	2	7	5.3	0	Pass	10	Pass
Test: pwrup_time																
Pwr-On_Rise_Time_Trise=	27	31	60	46	31	27	67	64	usec	27	67	44	15	Pass	50000	Pass
Power-On_Time_Tpon=	93.8	82	11.7	11.7	11.7	15.6	11.7	11.7	msec	11.7	93.8	31.2	0	Pass	400	Pass
Test: pwrup_inrush																
Init_Inrush=	430.13	429.25	428.88	430.13	431.63	431.63	430.5	430.38	mA	428.88	431.63	430.3	400	Pass	450	Pass
Max_Inrush_c4=	430.5	430	428.88	429.25	431.63	431.5	430	429.5	mA	428.88	431.63	430.2	400	Pass	450	Pass
Min_Inrush=	429	428.25	427.25	427.75	430.25	430	428	428.5	mA	427.25	430.25	428.6	400	Pass	450	Pass
T_inrush=	59.2	59.2	59.2	59.2	58.8	59.2	58	58.4	msec	58	59.2	58.9	50	Pass	75	Pass
Inrush_45ms=	54.6	54.7	54.7	54.7	54.6	54.7	54.7	54.7	Volts	54.6	54.7	54.7	50	Pass	57	Pass
Inrush_Voltage=	31.8	31.7	31.7	31.7	31.7	31.8	35.7	35.4	Volts	31.7	35.7	32.7	30	Pass	57	Pass
Max_Init_Inrush=	503.8	503.5	502.3	504.3	503.8	505.5	714	715	mA	502.3	715	556.5	0	Pass	2000	Pass
Inrush_Strategy_c4=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwrcon_v																
Vport_min_2=	53.6	53.7	53.8	53.8	53.7	53.8	53.8	53.8	V	53.6	53.8	53.8	50	Pass	57	Pass
Vport_max_2=	54.9	54.9	55	54.9	54.8	55	54.9	54.9	V	54.8	55	54.9	50	Pass	57	Pass
Vport_ripple_2=	180	180	183	183	181	180	202	191	mVpp	180	202	185	0	Pass	500	Pass
Vport_noise_2=	175	173	172	184	172	172	158	152	mVpp	152	184	169.8	0	Pass	200	Pass
Vtrans_min_2=	53.7	53.8	53.8	54.6	53.7	53.8	54.5	53.9	V	53.7	54.6	54	50	Pass	57	Pass
Vtrans_max_2=	54.8	54.9	55	55	54.9	54.9	55	54.9	V	54.8	55	54.9	50	Pass	57	Pass
Test: pwrcon_pwracap																
Pcon_c4=	32.7	32.6	32.6	32.7	32.7	32.6	32.6	32.7	watts	32.6	32.7	32.7	28.7	Pass	38.9	Pass
Icon_k_c4=	111.9	111.9	111.7	112	112	111.8	111.9	112.2	%	111.7	112.2	111.9	100	Pass	9999	Pass
Type-2_Enable=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pclass_LLDP_22_7=	1	1	1	1	1	1	0	0	****	0	1	1	1	Fail	1	Pass
Pclass_LLDP_24_5=	0	0	0	0	0	0	0	0	****	0	0	0	1	Fail	1	Pass
Test: pwrcon_maxi																
Ilim_Peak=	318.8	315	310	312	311.5	320.8	355.8	361.3	mA	310	361.3	325.7	0	Pass	1750	Pass
Ilim_Min_2=	686.8	686.3	684	686.8	686.8	684.8	685.8	686.3	mA	684	686.8	686	683	Pass	1750	Pass
Tlim_2=	61.3	61.3	60.5	60.2	61.3	60.9	61.3	60.2	msec	60.2	61.3	60.9	10	Pass	75	Pass
Vlim_2=	53.3	53.4	53.5	53.4	53.4	53.5	53.4	53.5	V	53.3	53.5	53.4	50	Pass	57	Pass
Ilim_Max_2=	861.3	860.5	858.3	861.3	861.3	859	860	860.8	mA	858.3	861.3	860.3	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	57.8	59.4	58.2	58.6	57.8	59	59	59	msec	57.8	59.4	58.6	10	Pass	9999	Pass
Ktran_lo_2=	107.5	107.6	107.7	107.8	107.5	107.7	107.8	107.8	%	107.5	107.8	107.7	92.4	Pass	115	Pass
Test: pwrcon_overld																
Ipeak_2=	125	125	125	125	125	125	125	125	%	125	125	125	0	Pass	125	Pass
Vport_Ipeak_2=	53.4	53.5	53.5	53.5	53.5	53.5	53.5	53.5	V	53.4	53.5	53.5	50	Pass	57	Pass
Vport_50DC_2=	53.4	53.5	53.5	53.5	53.4	53.5	53.5	53.6	V	53.4	53.6	53.5	50	Pass	57	Pass
Test: mps_dc_valid																
Min_Valid_Time_Tmps=	10	10	10	10	10	10	10	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1															